



TWELVE CHANNEL, 1MHz, 3000pF, CLOCK DRIVER



SCL Part No.

EF1102-0

PRODUCT DESCRIPTION:

The FT CCD detectors are envisaged to use for high-resolution application designs. Detector clocks requirements for external TDI operation are more complex due to large capacitive drive requirements. The clock drivers need to drive a high capacitive load $\sim 3000\text{pF}$. A 12 channel clock driver having four 3000pF channel and eight 150pF channel driving capabilities has been developed.

FEATURES:

- 3.3V power supply for VDD1
- 1 MHz operating frequency
- Four channels input
- Input level shifters
- $0.6\text{V} \geq V_L \geq 0\text{V}$
- $12\text{V} \leq V_H \leq 14\text{V}$
- Low power dissipation ($<5\text{W}$ at 1MHz)
- $t_R=60\text{ns}$, $t_F = 5 \text{ ns}$ at 3000pF load
- $t_R=6\text{ns}$, $t_F = 7 \text{ ns}$ at 150pF load
- $t_{PLH}=26\text{ns}$, $t_{PHL}=27\text{ns}$ delay
- Matched rise and fall time $<7\text{ns}$
- Matched propagation delay match $<2\text{ns}$
- Hermetic sealed 64 pin Lead Flat package (DIL)
- Operating Temperature: -55°C to $+125^\circ\text{C}$

